

# Abstracts

## CAD-oriented equivalent circuit modeling of on-chip interconnects for RF integrated circuits in CMOS technology

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*J. Zheng, Y.-C. Hahm, A. Weisshaar and V.K. Tripathi. "CAD-oriented equivalent circuit modeling of on-chip interconnects for RF integrated circuits in CMOS technology." 1999 MTT-S International Microwave Symposium Digest 99.1 (1999 Vol. 1 [MWSYM]): 35-38 vol.1.*

A CAD-oriented modeling methodology for coupled interconnects on lossy substrates in CMOS technology is presented. The modeling technique is based on a quasi-static EM analysis and an equivalent circuit model extraction and model-order reduction procedure. The response of the SPICE compatible equivalent circuit models is in good agreement with the frequency-dependent transmission line characteristics of the interconnects.

 [Return to main document.](#)